



PATENT APPLICATION  
Q-51885

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Akitaka KIMURA

Appln. No.: 09/161,981

Group Art Unit: 2812

Confirmation No.: 8269

Filed: September 29, 1998

Examiner: Mulpuri, S

For: SEMICONDUCTOR LAYER FORMED BY SELECTIVE DEPOSITION AND  
METHOD FOR DEPOSITING SEMICONDUCTOR LAYER

**PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136**

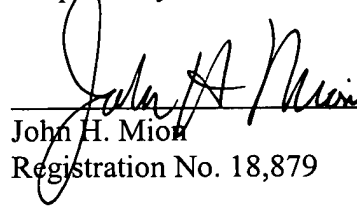
Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicant hereby petitions for an extension of time of three (3) months, extending the time for responding to the Office Action of February 26, 2002 to August 26, 2002.

A check for the statutory fee of \$920.00 is attached. Please charge any additional fees under 37 C.F.R. § 1.16 or § 1.17 necessary to keep this application pending in the Patent and Trademark Office or credit any overpayment to Deposit Account No. 19-4880. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

  
John H. Mion

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August 23, 2002

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